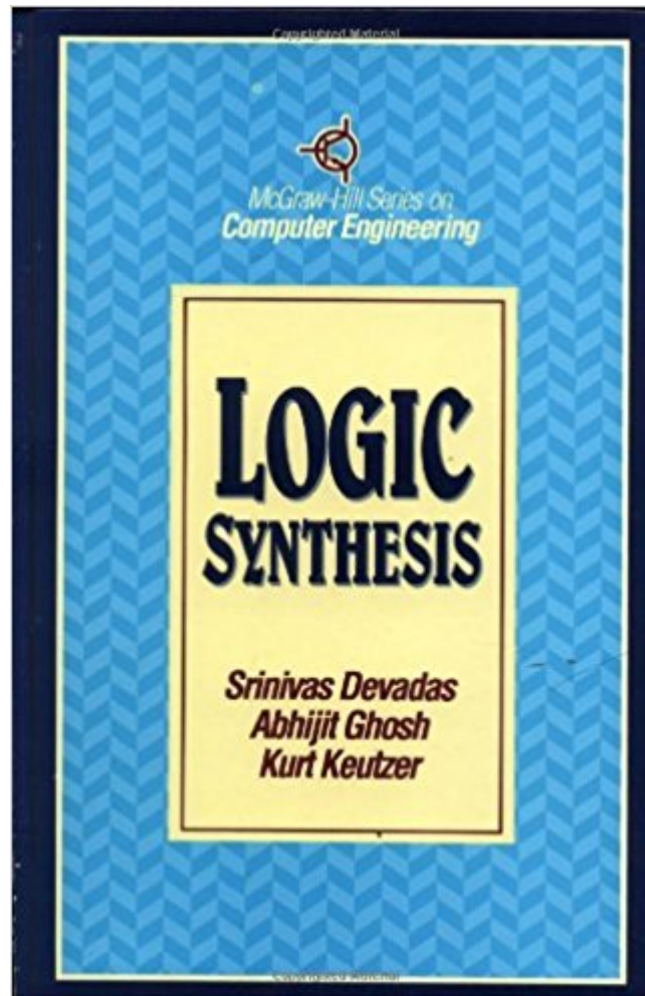




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# Logic Synthesis



## Synopsis

Logic synthesis enables VLSI designers to rapidly lay out the millions of transistors and interconnecting wires that form the circuitry on modern chips, without having to plot each individual logic circuit. This guide to logic synthesis techniques spotlights not only the synthesis of two-level, multi-level and combinational circuits, but also their testability.

## Book Information

Series: McGraw-Hill Series on Computer Engineering

Hardcover: 448 pages

Publisher: McGraw-Hill Professional; 1 edition (June 1, 1994)

Language: English

ISBN-10: 0070165009

ISBN-13: 978-0070165007

Product Dimensions: 6.3 x 1.2 x 9.3 inches

Shipping Weight: 1.8 pounds

Average Customer Review: 5.0 out of 5 stars 1 customer review

Best Sellers Rank: #3,194,145 in Books (See Top 100 in Books) #77 in Books > Engineering & Transportation > Engineering > Electrical & Electronics > Circuits > Logic #485 in Books > Computers & Technology > Programming > Software Design, Testing & Engineering > Logic #745 in Books > Textbooks > Engineering > Electrical & Electronic Engineering

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